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Applicant(s): Yoon-Min Kim, *et al.*
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Title: INTEGRATED CIRCUIT TEST SYSTEM AND METHOD

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10-22-03

Date

Vanessa Marakas

Vanessa Marakas

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to prosecution, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.